WHAT IS CLAIMED IS:

1. A microcomputer unit comprising a flash memory for storing specified data, a ROM storing a program for rewriting the specified data stored in said flash memory, a nonvolatile memory for storing version information and/or lot information of said flash memory, and a CPU for responding to an external command to execute the program for rewriting the specified data in said flash memory based on the version information and/or lot information.

2. The microcomputer unit as defined in claim 1, wherein said nonvolatile memory has a memory area to be used for storing parameters in the rewriting of the data in said flash memory by said CPU.

3. The microcomputer unit as defined in claim 1, wherein said nonvolatile memory has a memory area to be used for storing at least a part of a corrected program corrected from the program stored in said ROM.

4. A method for rewriting data stored in a flash memory installed in a microcomputer by using a CPU, the method comprising the steps of:

storing in the microcomputer a first program for rewriting the data stored in the flash memory;

storing version information and/or lot information of the flash memory in the microcomputer;

determining parameters for the rewriting based on the version information and/or lot information; and

running the first program on the CPU to rewrite the data in the flash memory based on the parameters.

5. The method as defined in claim 4, wherein the storing step stores the information in a nonvolatile memory.

6. The method as defined in claim 4, further comprising the steps of selecting a second program among a plurality of programs based on the version information and/or the lot information, and running the second program instead of the first program.